

Electronic Acknowledgement Receipt

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International Application Number:	
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Title of Invention:	Voltage generation circuit and semiconductor integrated circuit device
First Named Inventor/Applicant Name:	Keiko Fukuda
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File Listing:					
Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	NPL Documents	CNOOfficeAction.pdf	<div>669377</div> <div>b6b6bb6bb6b237f1717ba4c4027b05249d8a07</div>	no	14
Warnings:					
Information:					

2	Foreign Reference	CN86101789.pdf	544392 a1f16ee9c7bdc657a6b42805588f8b167aef28d	no	13
Warnings:					
Information:					
3		XA-10578_IDS.pdf	89633 67a1f995784de631e59f11ebc9638cb7108b3ca	yes	3
Multipart Description/PDF files in .zip description					
Document Description		Start	End		
Information Disclosure Statement Letter		1	2		
Information Disclosure Statement (IDS) Filed (SB/08)		3	3		
Warnings:					
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Total Files Size (in bytes):			1303402		
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